

FUNDAMENTALS OF DIGITALELECTRONICS - LOGIC GATES

By:

Maninder Jeet Kaur

Computer Department

For Class: BCA-I

Subject :Digital Electronics

Logic Circuits

- Gate
 - ▣ A device that performs a basic operation on electrical signals
- Circuits
 - ▣ Gates combined to perform more complicated tasks

Types of gates

- ▣ NOT
- ▣ AND
- ▣ OR
- ▣ XOR
- ▣ NAND
- ▣ NOR

NOT Gate

- ▣ A NOT gate accepts one input signal (0 or 1) and returns the opposite signal as output

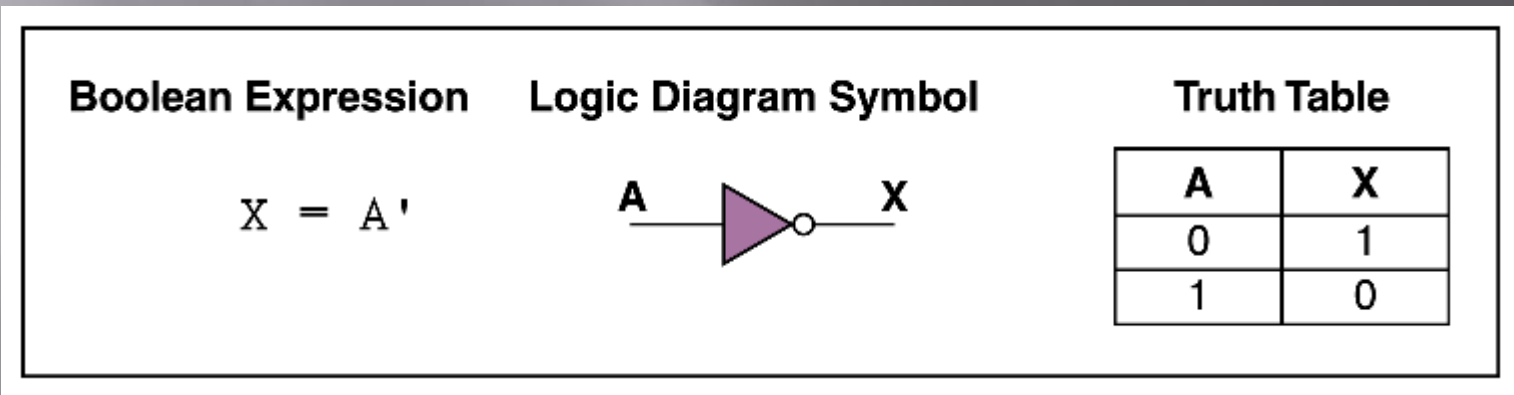


Fig: Representations of a NOT gate

AND Gate

- ▣ An AND gate accepts two input signals
- ▣ If both are 1, the output is 1; otherwise, the output is 0

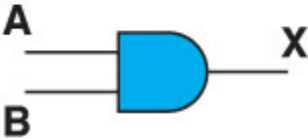
Boolean Expression	Logic Diagram Symbol	Truth Table															
$X = A \cdot B$		<table border="1"><thead><tr><th>A</th><th>B</th><th>X</th></tr></thead><tbody><tr><td>0</td><td>0</td><td>0</td></tr><tr><td>0</td><td>1</td><td>0</td></tr><tr><td>1</td><td>0</td><td>0</td></tr><tr><td>1</td><td>1</td><td>1</td></tr></tbody></table>	A	B	X	0	0	0	0	1	0	1	0	0	1	1	1
A	B	X															
0	0	0															
0	1	0															
1	0	0															
1	1	1															

Fig: Representation of an AND gate

OR Gate

- An OR gate accepts two input signals
If both are 0, the output is 0; otherwise,
the output is 1

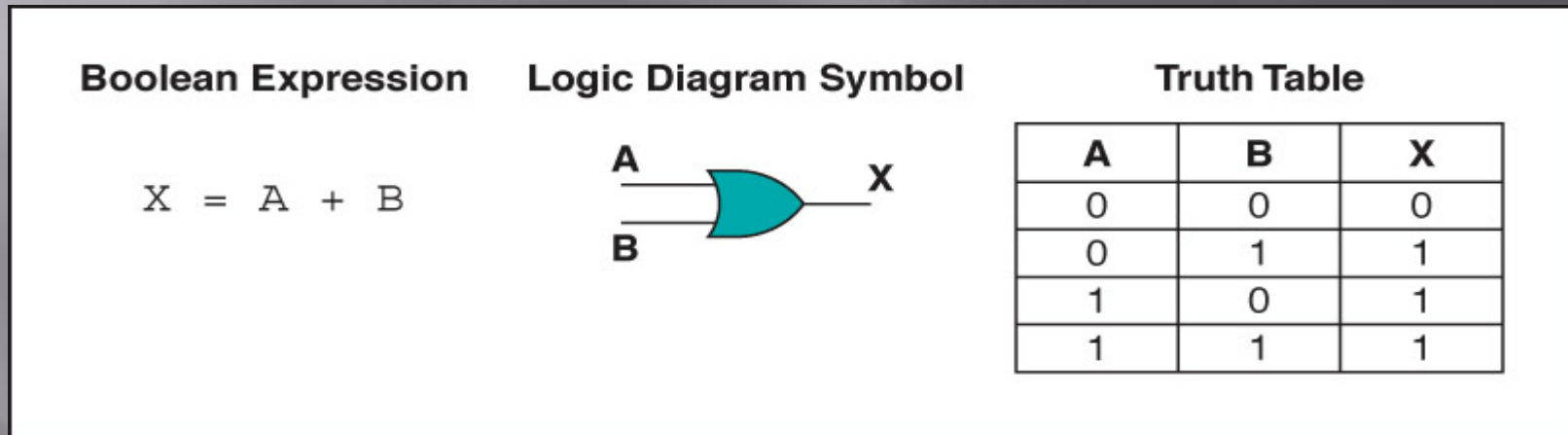


Fig:Representation of a OR gate

XOR Gate

- ▣ An XOR gate accepts two input signals
If both are the same, the output is 0; otherwise,
the output is 1

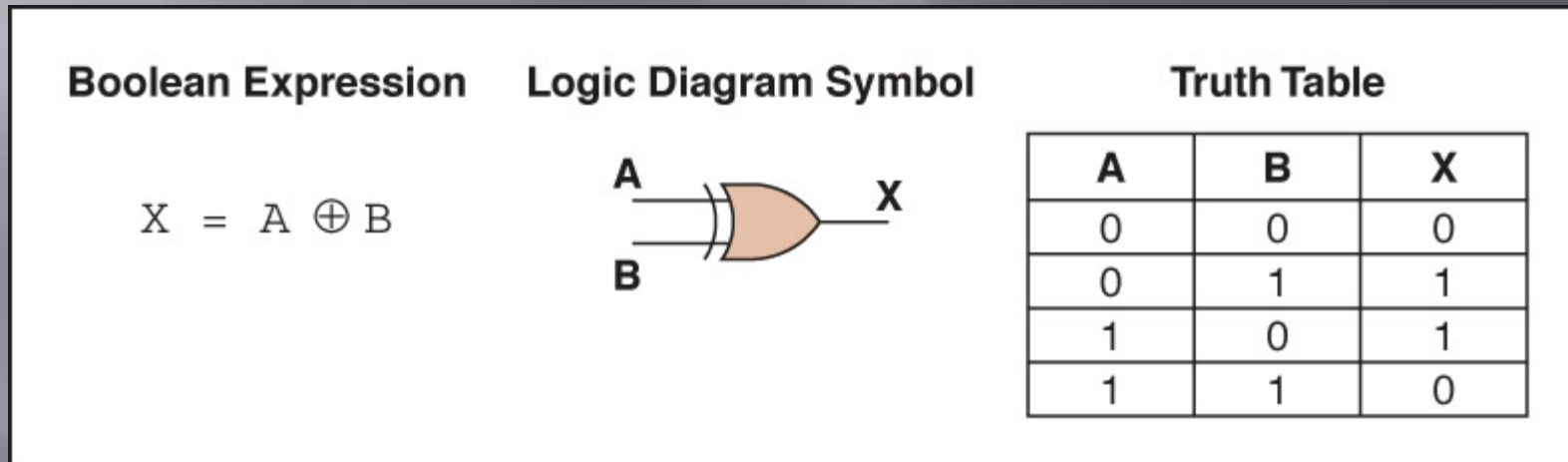


Fig:Representation of an XOR gate

NAND Gate

- ▣ The NAND gate accepts two input signals. If both are 1, the output is 0; otherwise, the output is 1.

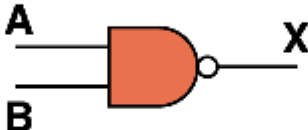
Boolean Expression	Logic Diagram Symbol	Truth Table															
$X = (A \cdot B)'$		<table border="1"><thead><tr><th>A</th><th>B</th><th>X</th></tr></thead><tbody><tr><td>0</td><td>0</td><td>1</td></tr><tr><td>0</td><td>1</td><td>1</td></tr><tr><td>1</td><td>0</td><td>1</td></tr><tr><td>1</td><td>1</td><td>0</td></tr></tbody></table>	A	B	X	0	0	1	0	1	1	1	0	1	1	1	0
A	B	X															
0	0	1															
0	1	1															
1	0	1															
1	1	0															

Fig: Representation of a NAND gate

NOR Gate

- ▣ The NOR gate accepts two input signals
If both are 0, the output the output is 0

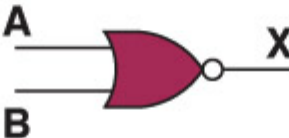
Boolean Expression	Logic Diagram Symbol	Truth Table															
$X = (A + B)'$		<table border="1"><thead><tr><th>A</th><th>B</th><th>X</th></tr></thead><tbody><tr><td>0</td><td>0</td><td>1</td></tr><tr><td>0</td><td>1</td><td>0</td></tr><tr><td>1</td><td>0</td><td>0</td></tr><tr><td>1</td><td>1</td><td>0</td></tr></tbody></table>	A	B	X	0	0	1	0	1	0	1	0	0	1	1	0
A	B	X															
0	0	1															
0	1	0															
1	0	0															
1	1	0															

Fig: Representation of a NOR gate

Thankyou